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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/534,931	05/13/2005	Shunpu Li	05-372	3671
20306 7590 01/19/2010 MCDONNELL BOEHNNEN HULBERT & BERGHOFF LLP 300 S. WACKER DRIVE 32ND FLOOR CHICAGO, IL 60606				
EXAMINER				
BROWN IL, DAVID N				
ART UNIT		PAPER NUMBER		
1791				
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01/19/2010		PAPER		

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

DETAILED ACTION

This is an after final response to the remarks dated 01/07/2010

Response to Arguments

1. Applicant's arguments filed 01/07/2010 have been fully considered but they are not persuasive.
2. Applicant's argument that the semiconductor and the substrate layers are not the same has been considered. It is not persuasive. The claim requires a substrate and a semiconductor. Since the substrate is a semiconductor, both of these claim requirements have been satisfied. The claim does not exclude the possibility that the two are one and the same. For this reason Chou teaches both a substrate and a semiconductor.
3. Applicant's argument that Chou does not teach a semiconductor on the polymer layer has been considered, but it is not persuasive. Chou teaches "onto the substrate 31 is a layered material...PMMA" [0035]. The mask or film is not being considered by the examiner as the substrate, rather the semiconductor layer.
4. Applicant's argument that modifying Chou to include additional layers would destroy the invention of Chou has been considered, but it is not persuasive. The polymer layer of Schaper is already textured when the metal layer is added. Therefore it would not interfere with the texturing.
5. Applicant's argument that one having ordinary skill in the art would not have been motivated to modify Chou to add Schaper's metal layer "by a desire to provide a coating for further development of the template" has been considered. It is not persuasive. The

semiconductor layer is already present. This is the semiconductor taught by Chou. It is underneath the polymer layer. The claim requires that either the semiconductor or the metal layer need be present. The semiconductive substrate is present according to Chou. Then the metal layer is added after the patterning of the polymer layer in the combined invention. Schaper indeed provides a motivation as to why one would be motivated to apply the metal layer (after patterning the polymer layer). Because the metal layer of Schaper is positioned on an already patterned polymer layer, such would not destroy the invention of Chou. It would have been obvious to one having ordinary skill in the art at the time of the invention to add an extra metallic layer to the template of Chou motivated by a desire to provide a coating for further development of the template.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to DAVID N. BROWN II whose telephone number is (571)270-5497. The examiner can normally be reached on Monday-Thursday 7:30a-5:00p EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Joseph Del Sole can be reached on (571)-272-1130. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/DAVID N. BROWN II/
Examiner, Art Unit 1791

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